

## Report on Alumni Talk

Organized by

Department of Electronics and Communication Engineering of  
Dr. Sudhir Chandra Sur Degree Engineering College

Date and Venue: 6<sup>th</sup> September 2025

No of Students Participation: 48

Speaker: Ms Rikta Pal

About the Speaker: Post Silicon Validation Engineer, Tata Semiconductor, ECE 2022 batch.

Mode: Online

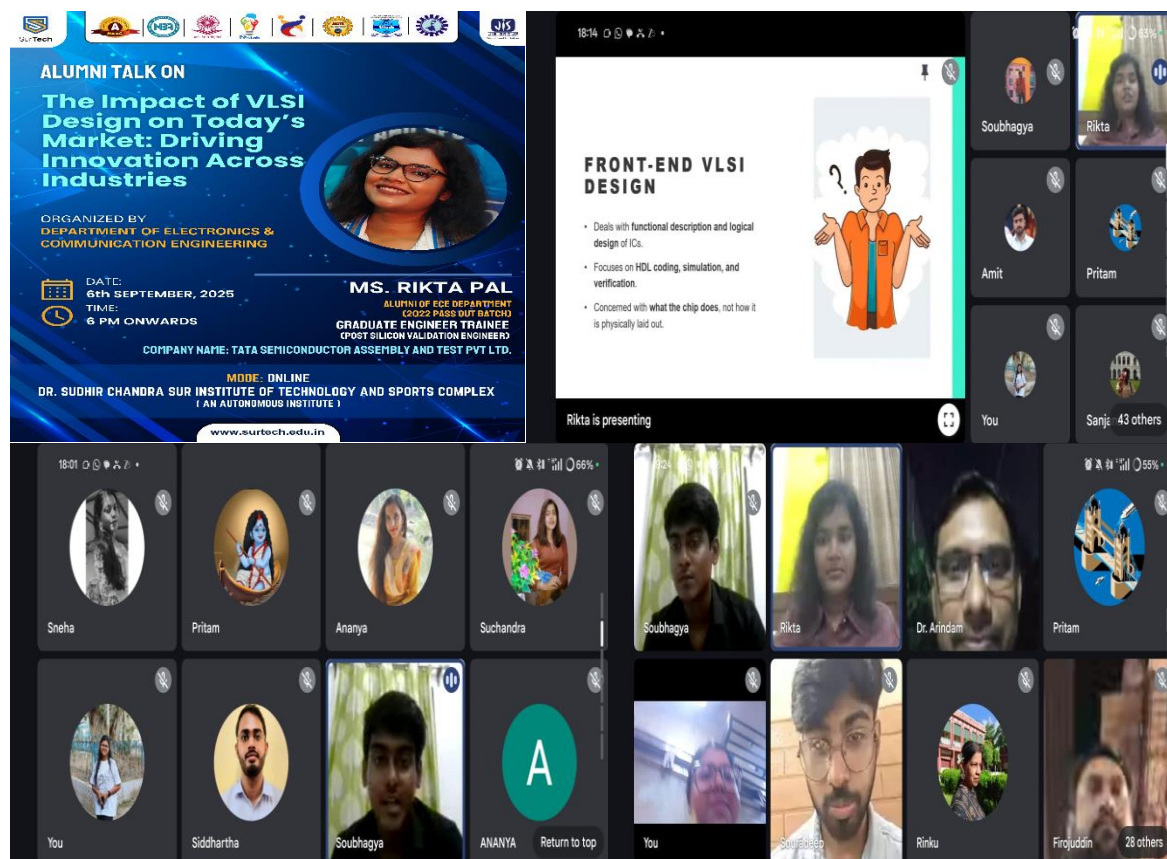
Department of ECE organised a one hour session on 'The Impact of VLSI Design on Today's Market: Driving Innovation Across Industries'. Ms Rikta Pal explained the different subjects of curriculum that will play important roles in VLSI industry. She explained the role of VHDL/Verilog in VLSI industry. She also emphasised students to go for TANER TOOL course for easy placement opportunity in VLI industry.

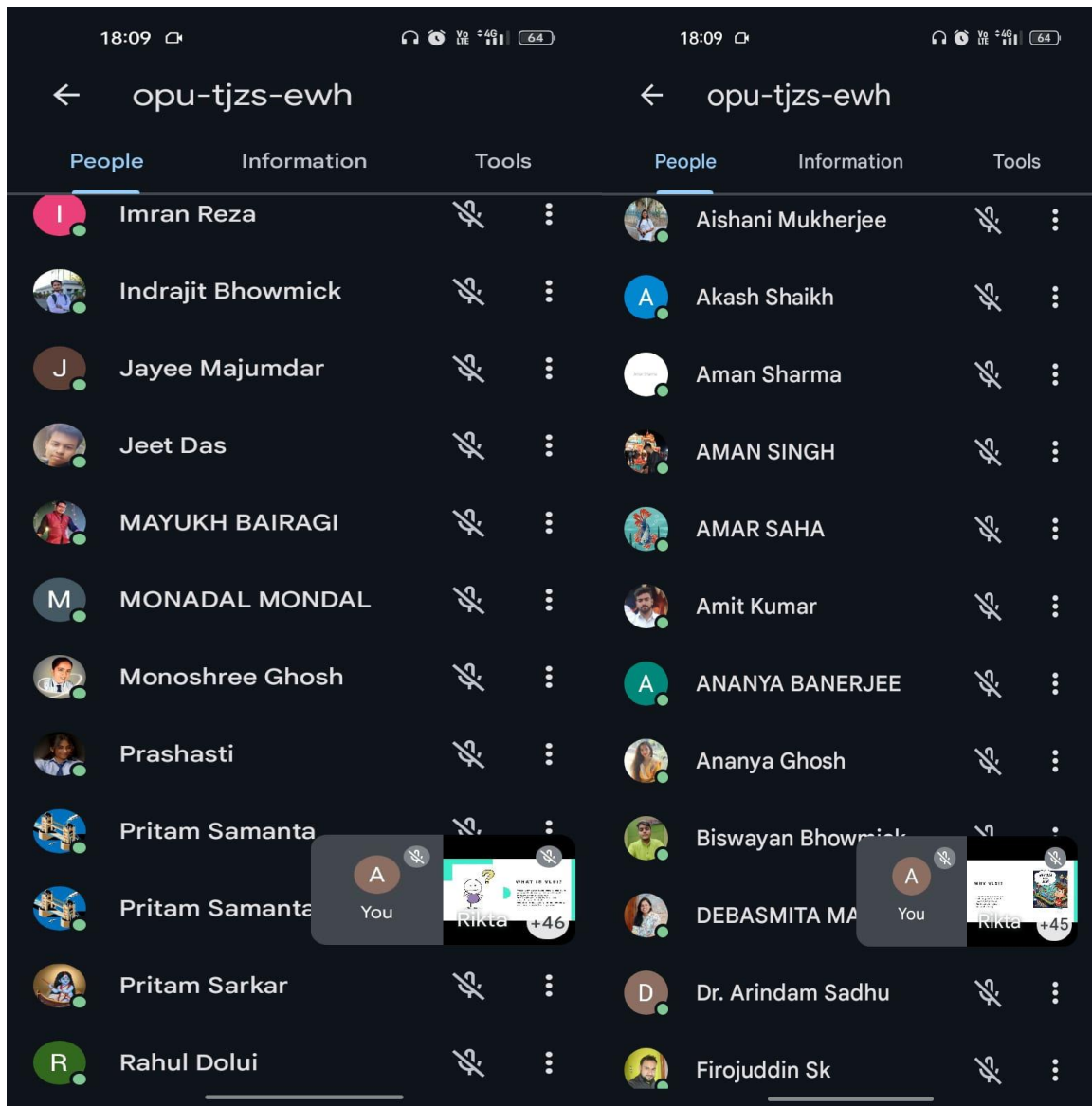
### Outcome of the Workshop:

Participants acquired the knowledge on the following:

1. How to start the preparation to join VLSI industry
2. Where are the scopes?
3. Some possible industry in Kolkata
4. Why to appear in GATE for some kind of depth knowledge in core domain.

Few glimpses of the event,





Dr Anirban Neogi  
HOD, ECE Department